

ABSTRACT

A low voltage differential signaling transmitting circuit includes a driver circuit that outputs currents as differential signals from its pair of output terminals in accordance with a digital signal input to its input terminal, a bias circuit that supplies the driver circuit with a bias current so as to adjust values of the currents output from the driver circuit, a current/voltage conversion circuit that converts the currents output from the pair of output terminals of the driver circuit into voltages, and a receiver circuit that compares the voltages with each other and outputs a signal representing a result of the comparison. The bias circuit and the current/voltage conversion circuit are arranged on a same semiconductor chip.

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